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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/669,261	09/25/2003	Seiji Matsumoto	027260-677	3208
21839	7590	03/30/2005	EXAMINER	
BURNS DOANE SWECKER & MATHIS L L P			NGUYEN, HAU H	
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ALEXANDRIA, VA 22313-1404			PAPER NUMBER	
			2676	

DATE MAILED: 03/30/2005

Please find below and/or attached an Office communication concerning this application or proceeding.

Office Action Summary

Application No.

10/669,261

Applicant(s)

MATSUMOTO, SEIJI

Examiner

Hau H Nguyen

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-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address --

Period for Reply

A SHORTENED STATUTORY PERIOD FOR REPLY IS SET TO EXPIRE 03 MONTH(S) FROM THE MAILING DATE OF THIS COMMUNICATION.

- Extensions of time may be available under the provisions of 37 CFR 1.136(a). In no event, however, may a reply be timely filed after SIX (6) MONTHS from the mailing date of this communication.
- If the period for reply specified above is less than thirty (30) days, a reply within the statutory minimum of thirty (30) days will be considered timely.
- If NO period for reply is specified above, the maximum statutory period will apply and will expire SIX (6) MONTHS from the mailing date of this communication.
- Failure to reply within the set or extended period for reply will, by statute, cause the application to become ABANDONED (35 U.S.C. § 133). Any reply received by the Office later than three months after the mailing date of this communication, even if timely filed, may reduce any earned patent term adjustment. See 37 CFR 1.704(b).

Status

- 1) ☒ Responsive to communication(s) filed on 25 September 2003.
- 2a) ☐ This action is **FINAL**. 2b) ☒ This action is non-final.
- 3) ☐ Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under *Ex parte Quayle*, 1935 C.D. 11, 453 O.G. 213.

Disposition of Claims

- 4) ☒ Claim(s) 1-7 is/are pending in the application.
- 4a) Of the above claim(s) _____ is/are withdrawn from consideration.
- 5) ☐ Claim(s) _____ is/are allowed.
- 6) ☒ Claim(s) 1-7 is/are rejected.
- 7) ☐ Claim(s) _____ is/are objected to.
- 8) ☐ Claim(s) _____ are subject to restriction and/or election requirement.

Application Papers

- 9) ☐ The specification is objected to by the Examiner.
- 10) ☐ The drawing(s) filed on _____ is/are: a) ☐ accepted or b) ☐ objected to by the Examiner.
Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a).
Replacement drawing sheet(s) including the correction is required if the drawing(s) is objected to. See 37 CFR 1.121(d).
- 11) ☐ The oath or declaration is objected to by the Examiner. Note the attached Office Action or form PTO-152.

Priority under 35 U.S.C. § 119

- 12) ☐ Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).
- a) ☐ All, b) ☐ Some * c) ☐ None of:
1. ☐ Certified copies of the priority documents have been received.
 2. ☐ Certified copies of the priority documents have been received in Application No. _____.
 3. ☐ Copies of the certified copies of the priority documents have been received in this National Stage application from the International Bureau (PCT Rule 17.2(a)).
- * See the attached detailed Office action for a list of the certified copies not received.

Attachment(s)

- | | |
|---|---|
| 1) <input checked="" type="checkbox"/> Notice of References Cited (PTO-892) | 4) <input type="checkbox"/> Interview Summary (PTO-413)
Paper No(s)/Mail Date. _____ |
| 2) <input type="checkbox"/> Notice of Draftsperson's Patent Drawing Review (PTO-948) | 5) <input type="checkbox"/> Notice of Informal Patent Application (PTO-152) |
| 3) <input checked="" type="checkbox"/> Information Disclosure Statement(s) (PTO-1449 or PTO/SB/08)
Paper No(s)/Mail Date <u>09/25/03</u> . | 6) <input type="checkbox"/> Other: _____ |

DETAILED ACTION

Claim Rejections - 35 USC § 102

1. The following is a quotation of the appropriate paragraphs of 35 U.S.C. 102 that form the basis for the rejections under this section made in this Office action:

A person shall be entitled to a patent unless –

(b) the invention was patented or described in a printed publication in this or a foreign country or in public use or on sale in this country, more than one year prior to the date of application for patent in the United States.

2. Claims 1-5, and 7 are rejected under 35 U.S.C. 102(b) as being anticipated by Dinwiddie et al. (U.S. Patent No. 5,959,687).

Referring to claims 1, 2, and 7, Dinwiddie et al. teach a system for processing auxiliary information, such as closed caption data, that is included in a television signal, wherein as shown in Fig. 3, after all characters needed to form a complete row of the closed caption display have been received and stored in RAM 116, CPU 112 transfers the data from RAM 116 to RAM 119 in OSD processor 117 via bus 111 (a memory bus). As can be seen in FIG. 3, RAM 119 is divided into two sections, namely RAM 371 and RAM 372 (first and second OSD RAMs). Each section will store one complete row (horizontal line) of closed caption character data. Data that is transferred to RAM 119 by CPU 112 is written into one section of RAM 119, e.g., RAM 371, that serves as a buffer while data is being read from the other section of RAM 119, e.g., RAM 372, to display a row of captioning. By the time that all data in the display section of RAM 119 has been read and displayed, the buffer section has been filled with new data. The buffer and display functions of the sections of RAM 119 are swapped and the next row of captioning is displayed. The swapping of RAM functions is accomplished by switching switch 373 (a switch)

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to couple data from bus 111 to buffer RAM 372 via node A2 of switch 373 while data is read from RAM 371 for display via node B2. Switch 373 is controlled by control unit 379 (OSD control circuit/ buffer transfer control circuit) in OSD processor 117 (col. 5, lines 13-40). As also shown in Fig. 3, Dinwiddie et al. shows a local bus for transferring data stored in RAMs 371 and 372 to be used for on screen display (OSD_RGB). Dinwiddie et al. further teach when all 16 bits of CC data have been loaded into registers 352 and 353, an interrupt flag is set in control unit 354 causing CPU 112 to read the bytes of data from the registers via bus 111 (col. 5, lines 4-12).

In regard to claims 3-5, as cited above, Dinwiddie et al. teach each OSD RAM 371 and 372 store a complete line of character data, and one is functioned as a display section, and the other is functioned as a buffer, and by the time that all data in the display section of RAM 119 has been read and displayed, the buffer section has been filled with new data. Therefore, the current horizontal scanning line is stored before making on screen display. Likewise, the next horizontal scanning line (row) is stored, the current horizontal scanning line has been sent to display.

Claim Rejections - 35 USC § 103

3. The following is a quotation of 35 U.S.C. 103(a) which forms the basis for all obviousness rejections set forth in this Office action:

(a) A patent may not be obtained though the invention is not identically disclosed or described as set forth in section 102 of this title, if the differences between the subject matter sought to be patented and the prior art are such that the subject matter as a whole would have been obvious at the time the invention was made to a person having ordinary skill in the art to which said subject matter pertains. Patentability shall not be negated by the manner in which the invention was made.

4. Claim 6 is rejected under 35 U.S.C. 103(a) as being unpatentable over Dinwiddie et al.

(U.S. Patent No. 5,959,687) in view of Kaneko (U.S. Patent No. 5,969,727).

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Referring to claim 6, as cited above, Dinwiddie et al. teach all the limitations of claim 6, except that the buffer comprises a dual port RAM.

However, Kaneko teaches an on-screen display unit, comprising a CPU 101 for carrying out overall control of the unit, a font ROM for storing font data, an OSD RAM for storing character codes and the like, and a line memory 100 consisting of a random access memory with a memory capacity corresponding to two horizontal scanning lines of the OSD unit. The line memory 100, having an arrangement like a dual port RAM or its equivalent, allows the CPU 101 to read or write through the data bus 110, and at the same time it can output its data DM to the selector 111 (col. 5, lines 15-47).

Therefore, it would have been obvious to one skilled in the art to utilize the method as taught by Kaneko in combination with the method as taught by Dinwiddie et al. in order to allows both inputting and outputting data at the same time (col. 5, lines 43-47).

Conclusion

5. The prior art made of record and not relied upon is considered pertinent to applicant's disclosure. See PTO-892 form.

Asano (U.S. Patent No. 6,351,291) discloses an image processing apparatus that can freely display onto an on-screen-display not only text information but also a natural image, comprising a buffer for storing OSD data read from an OSD memory.

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6. Any inquiry concerning this communication or earlier communications from the examiner should be directed to Hau H. Nguyen whose telephone number is: 571-272-7787. The examiner can normally be reached on MON-FRI from 8:30-5:30.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Matthew Bella can be reached on 571-272-7778.

Any response to this action should be mailed to:

Commissioner of Patents and Trademarks

Washington, D. C. 20231

or faxed to:

(703) 872-9306 (for Technology Center 2600 only)

Hand-delivered response should be brought to Crystal Park II, 2121 Crystal Drive, Arlington, VA, Sixth floor (Receptionist).

Any inquiry of a general nature or relating to the status of this application or proceeding should be directed to the Technology Center 2600 Customer Service Office whose telephone number is (571)-272-2600.

H. Nguyen

03/21/2005



Kee M. Tung
Primary Examiner